

Digital Electronics (SKEE1223) Karnaugh Maps

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Introduction

- A logic circuit can be built using logic gates connected according to its Boolean function.
- This type of circuit is known as combinational logic circuit.
- The best design is the simplest circuit derived from the simplest Boolean expression.
- Simplifying a Boolean function allows us to remove redundant gates.







Logic Simplification

- Boolean Algebra
 - Not guaranteed to obtain simplest solution
- Karnaugh Maps
 - Introduced by Maurice Karnaugh
 - A graphical method of writing the truth table
 - Quickly simplify Boolean functions up to 6 variables



Karnaugh Maps for 2-4 Variables

• Number of cells = 2^n , where n = number of inputs



OUTM













 $F(A,B,C) = B.\overline{C}$























OUTM



4-variable K-map



 $F(A,B,C,D) = B.\overline{C} + \overline{B}.C.D + \overline{A}.C.D$ $= B.\overline{C} + \overline{B}.C.D + \overline{A}.B.D$













Examples of Illegal Loops

UTMONLINELEARNING









Examples of Illegal Loops

UTMONLINELEARNING









Looping 0's for POS Expressions



 $F(A,B,C) = (\overline{A} + \overline{B}).(\overline{B} + \overline{C})$









Looping 0's for POS Expressions









Don't Care Conditions

- Certain input combinations do not exist or are not allowed.
- They are labelled as X in the K-map.
- X can be assumed to be either 0 or 1.
- Can be used to simplify Boolean function by forming larger loops of 1's or 0's.
- It is unnecessary to loop the unused "don't cares"







Don't Care Conditions



 $F(A,B,C,D) = B.\overline{C}+C.D$

